

4Gb DDR4 SDRAM

Lead-Free&Halogen-Free
(RoHS Compliant)

CT52V1GM4C4-xx

CT52V512M8C4-xx

CT52V512M8C4-xxI

CT52V256M16C4-xx

CT52V256M16C4-xxI

Revision History

Revision No.	History	Draft Date	Remark
0.1	Initial Release	Apr. 2016	
0.2	Add Industrial Temperature Range	Oct. 2016	
1.0	x16 IDD, IPP Update	Dec. 2016	Page 32, 33
1.1	x16 IPP Typo	Dec. 2016	Page 33
1.2	x8 IDD, IPP Update	Jan. 2017	Page 32, 33
1.3	DBI Info. Update	Mar. 2017	Page 3
1.4	x4 IDD, IPP Update x16 3200Mbps IDD, IPP Update	Jun. 2017	Page 32,33
1.5	3200Mbps IDD, IPP Update	Jun. 2017	Page 32,33
1.6	AC timing update 2666Mbps update	Dec. 2017	Page 17
1.7	3200Mbps Parameter update	Mar. 2018	

如需获取完整版规格书
请联系我司岳经理
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Linda

